

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. PATENT DATE

SCANNED NS1 Q.A. 7/11

APPLICATION NO. 09/975202	CONT/PRIOR	CLASS 321 710	SUBCLASS 305	ART UNIT 2816 2181	EXAMINER Binehart Vu
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Koji Ishii

Method and apparatus for sharing signal pins on an interface between a system controller and peripheral integrated circuits

PTO-2040
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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
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			Amount Due	Date Paid
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